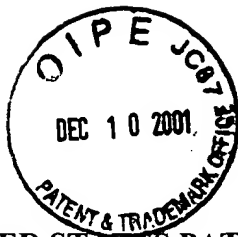


Docket No.: 57454-012



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Tsukasa OOISHI, et al.

Serial No.: ~~09/781,238~~

Group Art Unit: 2811

Filed: February 13, 2001

Examiner:

For: SEMICONDUCTOR MEMORY DEVICE INCLUDING PLURALITY OF GLOBAL  
DATA LINES IN PARALLEL ARRANGEMENT WITH LOW PARASITIC  
CAPACITANCE, AND FABRICATION METHOD THEREOF

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents  
Washington, DC 20231

Sir:

Attached is a copy of the Filing Receipt received from the U.S. Patent and Trademark Office in the above-referenced application. It is noted that the Residence of the Inventor, and Total Number of Claims is incorrect on the Filing Receipt. Attached is a copy of the Combined Declaration/Power of Attorney being filed concurrently herewith, which evidences that the Residence of the Inventor should read: SUNNYVALE, CALIFORNIA. The Total Number of Claims should read: 36. It is requested that a corrected filing receipt be issued.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

Michael E. Fogarty  
Registration No. 36,139

600 13<sup>th</sup> Street, N.W.  
Washington, DC 20005-3096  
(202) 756-8000 MEF:ykg  
Date: December 10, 2001  
Facsimile: (202) 756-8087



## UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS  
UNITED STATES PATENT AND TRADEMARK OFFICE  
WASHINGTON, D.C. 20231  
www.uspto.gov

APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/917,238	07/30/2001	2817	0.00	57457-012	29 ✓	3736	6 ✓

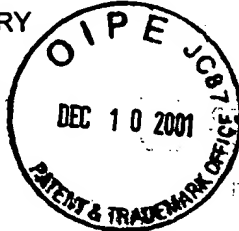
CONFIRMATION NO. 8950

## FILING RECEIPT



\*OC000000006533042\*

MCDERMOTT, WILL & EMERY  
600 3th Street, N.W.  
Washington, DC 20005-3096



Date Mailed: 09/10/2001

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

Shoichi Yoshizaki, Residence Not Provided;

*Sunnyvale, California*

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted 09/07/2001

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

## Title

Method and apparatus for recovering a clock signal from an asynchronous data signal ✓

## Preliminary Class

331

Data entry by : TRAN, TUAN

Team : OIPE

Date: 09/10/2001



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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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- The articles such as "a," "an" and "the" are not included as the first words in the title of an application. They are considered to be unnecessary to the understanding of the title.
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- The title may be truncated if it consists of more than 500 characters (letters and spaces combined).
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